

ELECTRONICS, TELECOM AND SOFTWARE PATENT PRACTICE UPDATE

Wednesday November 9, 2011 12:00 - 5:15 p.m.

New Brunswick Hyatt

Two Albany Street

New Brunswick, NJ, 08901

CLE: 4.0 NJ/PA

Fee: \$150 by November 4th / \$165 after November 4th

Registration: Register online at https://www.njipla.org/event_registration.asp?eventID=42

Program

12:00-12:45 p.m. **Lunch**

12:45-12:50 p.m. **Opening Remarks**
Andrea Kamage, NJIPLA President, Johnson & Johnson, New Brunswick, NJ

12:50-2:00 p.m. **The Prosecution of Patent Applications in the Electrical Arts**

12:50-1:20 p.m. **Examination of Applications on Electrical and Software Inventions**
James Dwyer, Assistant Deputy Commissioner, Computer Architecture, Software, Networking and Multiplex Communications Arts, USPTO, Washington, DC

1:20-1:40 p.m. **Impact of the New Legislation on Prosecution of Patent Applications in the Electrical Arts**
Robert Rudnick, Gibbons, Newark, NJ

1:40-1:55 p.m. **Argument of an Appeal before the BPIA**
Joel Miller, West Orange, NJ

1:55-2:00 **Q and A**

2:00-2:10 p.m. **Break**

- 2:10-3:50 p.m. **Electrical, Telecom and Software Patent Law Update**
- 2:10-2:25 p.m. **Patent Eligible Subject Matter in Software Inventions Post-Bilski**
Gregory Murgia, Alcatel-Lucent, Murray Hill, NJ
- 2:25-2:40 p.m. **Claim Construction and the DOE in the Electrical Arts**
Michelle Conover, Siemens Corporation, Princeton, NJ
- 2:40-2:55 p.m. **Obviousness of Software and Electrical Inventions Post-KSR**
Jon Chiodo, Hoffmann & Baron, Parsippany, NJ
- 2:55-3:10 p.m. **Joint Infringement of Claims Drawn to Method and System Inventions**
Davy Zoneraich, Lerner, David, Littenberg, Krumholtz & Mentlik, Westfield, NJ
- 3:10-3:25 p.m. **Apportionment of Patent Infringement Damages on Electrical Inventions**
Anthony S. Volpe, Volpe and Koenig, Philadelphia, PA
- 3:25-3:40 p.m. **Technical Standards and Patents - Conceptions and Myth Conceptions**
Marc Sandy Block, IBM Corporation, Armonk, NY
- 3:40-3:50 p.m. **Q and A**
- 3:50-4:00 p.m. **Break**
- 4:00-5:10 p.m. **Litigation of Electrical, Telecom and Software Patents Before the USITC**
- 4:00-4:30 p.m. **A View from the Bench**
Honorable Robert K Rogers, Jr., Administrative Law Judge USITC, Washington, DC
- 4:30-4:45 p.m. **Practical Realities of Litigating Patents on Electrical Inventions in the ITC**
Mark Abate, Goodwin Procter, New York, NY
- 4:45-5:00 p.m. **Proving a “Violation” in the ITC**
Tony Pezzano, Cadwalader, Wickersham & Taft, New York, NY
- 5:00-5:10 p.m. **Q and A**
- 5:10-5:15 p.m. **Closing Remarks**
Robert Rudnick, Gibbons, Newark, NJ